TEQIP – III Sponsored
Short Term Training Programme (STTP)
On
"DESIGN & IMPLEMENTATION OF DSP APPLICATIONS in XILINX, ALTERA, CADENCE AND SYNOPSYS EDA TOOLS"
REGISTRATION FORM

1. Name: .....

2. ☐ Student ☐ Research Scholar ☐ Faculty

3. Department: .....

4. Address for Communication:

.....

.....

.....

5. Mobile No: .....

6. Mail ID: .....

7. Is accommodation required? .....

PAYMENT DETAILS:

Name of the Bank :
Amount :
Demand Draft Number :
Date :

Signature & Seal
(Head of Institution)

REGISTRATION FEE

Participants are requested to register by filling in the accompanying slip and sending the same to the course coordinator.

For UG &PG Students : Rs. 1000

For Research Scholars & Faculties : Rs. 2000

Demand Draft should be drawn in favour of "**IRG NIT NAGALAND**" from any nationalized bank payable at **Nagaland** and should be sent to the coordinator.

Maximum number of participants: **25**
(First come first served basics)

IMPORTANT DATES:

Last date of Registration : 10.02.2018

Intimation of Selection : 12.02.2018

Programme Date : 14th - 18th Feb'18

PARTICIPATION IS OPEN TO:

- ✓ UG & PG Students
- ✓ Research Scholars
- ✓ Faculties

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On
"DESIGN & IMPLEMENTATION OF DSP APPLICATIONS in XILINX, ALTERA, CADENCE AND SYNOPSYS EDA TOOLS"

14th to 18th Feb, 2018

CONVENOR

Dr. G. Seetharaman

COORDINATORS

Dr. P. Chinnamuthu
Dr. Debadatta Pati
Dr. Jay Chandra Dhar
Dr. Naorem Khelchand Singh

Organized by
Department of Electronics and Communication Engineering

NATIONAL INSTITUTE OF TECHNOLOGY
CHUMUKEDIMA, DIMAPUR
NAGALAND – 797 103.
www.nitnagaland.ac.in

ABOUT THE INSTITUTION

National Institute of Technology Nagaland, it is one among the ten newly approved NITs by the Government of India in 2009 under the 11th Five Year Plan and it started functioning from the academic year 2010, is dedicated to the cause of quality education in the field of Engineering & Technology in Nagaland. Our Vision is to advance knowledge through quality education and research, to cultivate invention improving the human condition and to educate students for a life of professional achievement, service to society and individual fulfilment – moving our world towards a more sustainable path. NIT Nagaland offers Under Graduate studies in 6 disciplines, Post Graduate in 4 disciplines and Doctoral Programmes in all disciplines. All departments have well equipped laboratories in addition to the common facilities of workshops, central library, state-of-art central computing facility, sports facilities.

ABOUT THE DEPARTMENT

As the world calls out for answers to questions more complex than ever before, the Department of Electronics and Communication Engineering of NIT Nagaland generates new knowledge by engaging in cutting-edge research and to promote academic growth by offering state-of-the-art technology in undergraduate, postgraduate and doctoral programmes. In this inspired place, where every field of learning is in close proximity to each other, those solutions can be delivered on campus and around the globe by supporting the right projects and partnerships.

The Department of Electronics and Communication Engineering of NIT Nagaland is uniquely equipped, and obliged to educate the leaders the world needs now. The department is well equipped with laboratories and have excellent teaching staffs to nurture the innovative minds. Our vision is to lay the groundwork in shaping the future in the fields of Electronics and Communication Engineering and serve the nation by providing excellent technical education and in conducting high quality fundamental and applied research.

TEQIP-III INTRODUCTION

The Project, third phase of Technical Education Quality Improvement Programme (referred to as TEQIP-III) is fully integrated with the Twelfth Five-year Plan objectives for Technical Education as a key component for improving the quality of Engineering Education in existing institutions with a special consideration for Low Income States and Special Category States (SCS) and support to strengthen few affiliated technical universities to improve their policy, academic and management practices.

PROJECT OBJECTIVES:

The Project will focus on the following objectives:

Improving quality and equity in engineering institutions in focus states viz. 7 Low Income States (LIS), eight states in the North-East of India, three Hill states viz. Himachal Pradesh, Jammu & Kashmir, Uttarakhand and Andaman and Nicobar Islands (a union territory),

System-level initiatives to strengthen sector governance and performance which include widening the scope of Affiliating Technical Universities (ATUs) to improve their policy, academic and management practices towards affiliated institutions, and Twinning Arrangements to Build Capacity and Improve Performance of institutions and ATUs participating in focus states.

PROGRAMME

The aim of the STTP is to strengthen the knowledge in the field of VLSI Design for Digital Signal Processing Applications and its various implementation methods. Also to provide practical session using XILINX, ALTERA, and CADENCE & SYNOPSYS EDA Tools.

RESOURCE PERSONS

Experts from top Institutions and Industry will impart their expertise with hands on training.

ADDRESS FOR COMMUNICATION

The Convenor
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National Institute of Technology,
Chumukedima, Dimapur,
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Mobile: 9486631181